

MAR 08 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chambers
Serial No: 09/902,051
Filed: 07/10/01
For: CMOS DC OFFSET CORRECTION CIRCUIT WITH PROGRAMMABLE
HIGH-PASS TRANSFER FUNCTION

Docket No: TI-30883
Examiner: Davidson, Dan
Art Unit: 2651

OFFICIAL

#9
3-11-04

RESPONSE

Assistant Commissioner For Patents
Washington, DC 20231

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being
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Office at 703-872-9306 on 3-8-04:

Tommy Chambers
Tommy Chambers

Dear Sir:

Responsive to the Office Action mailed December 8, 2003 in connection with the
above identified application, Applicants respectfully submit the following remarks.

To: Technology Unit: 2651
Facsimile Number: 703-872-9306

Total Pages Sent 4

From: W. Daniel Swayze, Jr.
Texas Instruments Incorporated
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NAME OF INVENTOR(S): Chambers	
RECEIPT DATE & SERIAL NO.: Serial No.: 09/902,051 Filing Date: 7/10/2001	
TITLE OF INVENTION: CMOS DC OFFSET CORRECTION CIRCUIT WITH PROGRAMMABLE HIGH-PASS TRANSFER FUNCTION	
TI FILE NO.: TI-30883	DEPOSIT ACCT. NO.: 20-0668
FAXED: 03/08/2004 DUE: 02/08/2004 ATTY/SECY: WDS/tlc	

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